

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination BURGER ET AL.	
		Examiner Robert E. Fennema	Art Unit 2183	Page 1 of 1

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*	B	US-5,241,635	08-1993	Papadopoulos et al.	712/201
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	M	US-			

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NON-PATENT DOCUMENTS

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	U	Fisher, Joseph. "Trace Scheduling: A Technique for Global Microcode Compaction". IEEE Transactions on Computers, Volume C30, Number 7. July, 1981.
	V	Requa, Joseph. McGraw, James. "The Piecewise Data Flow Architecture: Architectural Concepts". IEEE Transactions on Computers, Volume C32, Number 5. May, 1983.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.